

L Number	Hits	Search Text	DB	Time stamp
1	18	714/22,24.ccls. and delay same (initializ\$6 or config\$6)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/21 15:28
-	46	((power adj failure) or reset) and (retention adj state)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/21 14:35
-	1	((power adj failure) or reset) same initial\$6 same (retention adj state)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/16 17:03
-	34	((power adj failure) or reset) and initial\$6 and (retention adj state)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/16 17:05
-	2	((power adj failure) or reset) and initial\$6 and (retention adj state) and (memory adj controller)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/16 17:07
-	21	(memory adj system) and (retention adj state)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/16 17:07
-	62	memory same (retention adj state)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/16 17:13
-	2	714/\$.ccls. and memory same (retention adj state)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/16 17:07
-	42	memory same (retention adj state) and initial\$6	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/16 17:14
-	2	(memory adj controller) same (retention adj state) and initial\$6	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/20 15:07
-	2	(memory adj controller) same (retention adj state)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/16 17:15
-	6	(memory adj controller) same (delaying adj signal)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/20 09:13
-	6571	(reset adj signal) same delay	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/20 09:24
-	4048	delay\$3 with (reset adj signal)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/20 09:26

-	15	(memory adj controller) same (delay\$3 with (reset adj signal))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/20 09:30
-	382	(delay\$3 adj (reset adj signal))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/20 09:52
-	2	(failure or fault or error) same memory same (delay\$3 adj (reset adj signal))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/20 09:31
-	34	(delay\$3 adj (reset adj signal)) same initial\$6	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/20 10:49
-	373	(reset\$4 or (power adj (failure or error or fault))) same delay\$3 same (initial\$6 with memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/20 11:09
-	6	(retention adj state) same delay	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/20 11:29
-	314	(detect\$3 adj power adj failure) and reset\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/20 11:19
-	162	(detect\$3 adj power adj failure) and reset\$3 and delay\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/20 11:24
-	3	(detect\$3 adj power adj failure) same reset\$3 same delay\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/20 11:24
-	261	retention adj state	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/20 11:29
-	382	(delay\$3 adj (reset adj signal))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/20 15:16
-	2	(retention adj state) and ((delay\$3 adj (reset adj signal)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/20 13:41
-	8	(power adj failure) and (delay\$3 adj (reset adj signal))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/20 13:49
-	2	(memory adj controller) same (delay\$3 adj reset adj signal)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/20 15:08

